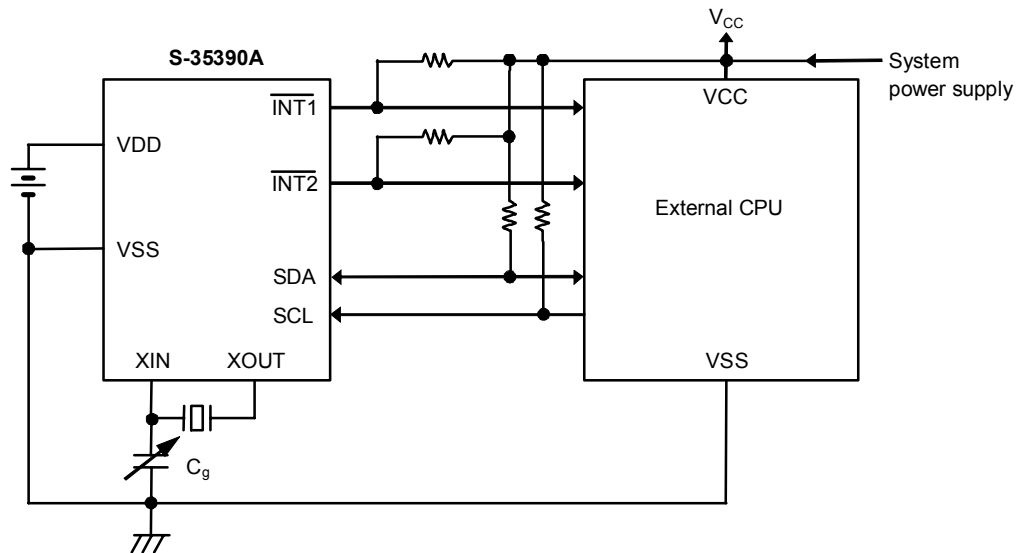
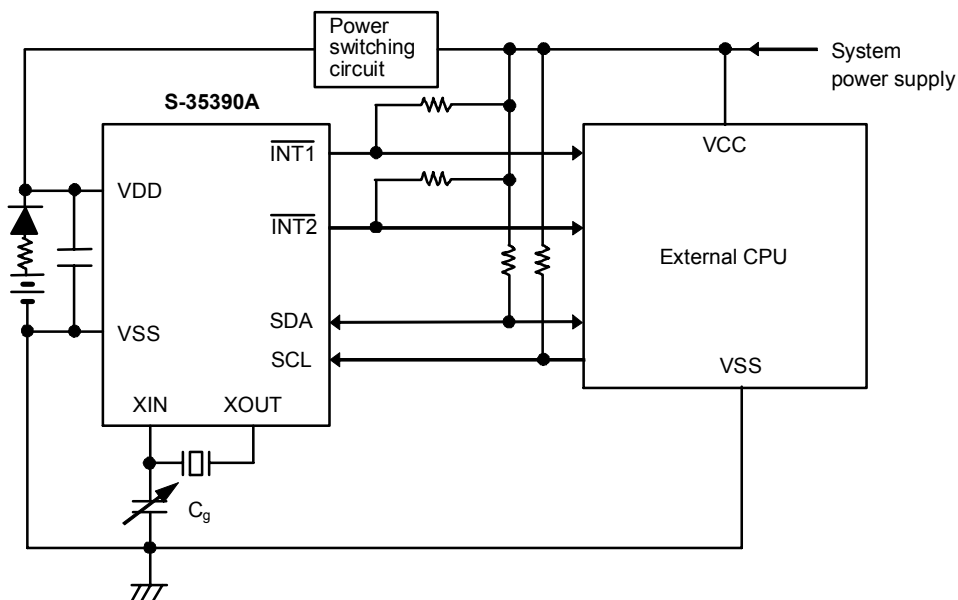


■ Examples of Application Circuits



- Caution**
1. Because the I/O pin has no protective diode on the VDD side, the relation of $V_{CC} \geq V_{DD}$ is possible. But pay careful attention to the specifications.
 2. Communication should be executed after the system power supply is turned on and a stable state is obtained.

Figure 34 Application Circuit 1



- Caution** Communication should be executed after the system power supply is turned on and a stable state is obtained.

Figure 35 Application Circuit 2

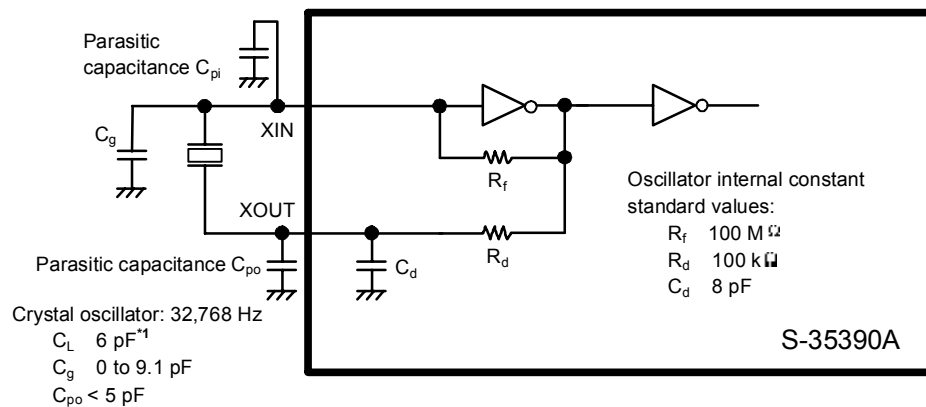
- Caution** The above connection diagrams do not guarantee operation. Set the constants after performing sufficient evaluation using the actual application.

■ Adjustment of Oscillation Frequency

1. Configuration of oscillator

Since crystal oscillation is sensitive to external noise (the clock accuracy is affected), the following measures are essential for optimizing the oscillator configuration.

- (1) Place the S-35390A, crystal oscillator, and external capacitor (C_g) as close to each other as possible.
- (2) Increase the insulation resistance between pins and the substrate wiring patterns of XIN and XOUT.
- (3) Do not place any signal or power lines close to the oscillator.



*1. When using the crystal oscillator with a C_L value of 7 pF, externally connect C_d .

Figure 36 Connection Diagram 1

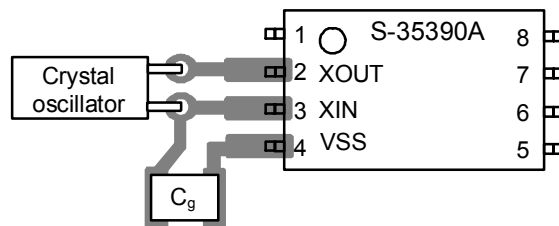


Figure 37 Connection Diagram 2